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Abstract of the Disclosure

A solid-state camera device having a matrix of pixels arranged in rows and columns, wherein a subset of pixels of the entire pixel matrix can be read for display and confirmation of photographic conditions to reduce power consumption and decrease time to display the image. In electronic cameras light is focused on the camera device having pixels and the light energy is used to provide an electric signal from each pixel in proportion to the incident light to provide an image signal. The image signal may be displayed on an attached display such as a liquid crystal display. Because most display have fewer pixels than the camera device, the display can not display image information from each camera device pixel. To thin the number of pixels provided to the display, at least one of a vertical shift register or horizontal shift register includes a selector circuit whereby the respective shift register may select a group of rows or columns, respectively, and the selector circuit can select a single row or column, respectively, from the group. Control signals control the operation of the shift registers and selector circuit(s) and permit selection of desired thinning schemes to optimize the display of image information for best display of colors or minimum energy consumption, as desired.